U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

DECLARATION

ATTOMETY BOCKET NO. 2885/34

As a below named inventor, I bereby declare that:

My residence, post office address, and citizenship are as estated below next to my name.

Ibelieve am an original, first, and joint inventor of the subject maner that is claimed and for which a patent is sought on the invention emitted METHOD OF HIERARCHICAL CACHING OF CONFIGURATION DATA HAVING DATAFLOW PROCESSORS AND MODULES BAVING TWO-OR MULTIDIMENSIONAL PROGRAMMIABLE CELL STRUCTURE (FPGAs, DPGAs, etc.), for which an application for Letters Rown was filed in the United States Frient and Trademark Office on August 25, 2000, Serial No. 09/623,052, which is the U.S. national phase of International Application PCT/DE99/00304 filed Petruary 25, 1809.

I bereby state that I have reviewed and understand the contents of the above identified specification, including the claims.

I acknowledge the duty to disclose information which is material to the examination of this application id accordance with Title 31, Code of Pederal Regulations, § 1.56(a).

PRIOR LINITED STATES APPLICATION(B)

I hereby elaim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner growided by the first paragraph of Title 35. United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37. Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

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PRIOR FOREIGN APPLICATION(S)

I hereby claim foreign priority benefits under Title 35, United States Code, §1 9 of any foreign application(x) for patent or inventor's certificate listed below and have also identified below any foreign application for parent or inventor's certificate having a filing date before that of the application on which priority is claimed:

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APPOINTMENT OF POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST

PACT INFORMATIONSTECHNOLOGIE GmbH as assignee of the entire right, title, and interest in the application for patent entitled METHOD OF HIERARCHICAL CACHING OF CONFIGURATION DATA HAVING DATAFLOW PROCESSORS AND MODULES HAVING TWO- OR MULTIDIMENSIONAL PROGRAMMABLE CELL STRUCTURE (FPGAs, DPGAs, etc.), for which an application for Letters Patent was filed in the United States Patent and Trademark Office on January 9, 2001, Serial No. 09/623,052, does hereby appoint Edward J. Handler, III (Registration No. 25,597), Michelle M. Carniaux (Registration No. 36,098) and Andrew L. Reibman (Registration No. 47,893), as my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Please address all communications regarding this application to:

Michelle M. Carniaux, Esq. KENYON & KENYON One Broadway New York, New York 10004

Please direct all telephone calls to Michelle M. Carniaux at (212) 425-7200.

PACT INFORMATIONSTECHNOLOGIE GmbH

Leopoldstr. 236 80807 München

Federal Republic of Germany

Dated: 23 nd may 2002

By:_____

Martin Vorbach

Position: CO

PACT INFORMATIONSTECHNOLOGIE GmbH